

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
	)	
Drew D. Perkins et al.	)	Group Art Unit: 2616
	)	
Serial No.: 10/712,732	)	Examiner: Brenda H. Pham
	)	
Filed: November 12, 2003	)	
	)	Confirmation No.: 9275
For: METHOD AND APPARATUS FOR	)	
DEPLOYING FORWARD ERROR	)	
CORRECTION IN OPTICAL	)	
TRANSMISSION NETWORKS	)	
AND THE DEPLOYMENT OF	)	
PHOTONIC INTEGRATED CIRCUIT	)	
(PIC) CHIPS WITH THE SAME	)	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RESPONSE TO RESTRICTION REQUIREMENT**

In a restriction requirement having a Notification Date of March 27, 2008 (the response to which having been extended through June 27, 2008, by a payment of a two-month extension of time fee submitted contemporaneously herewith), the Examiner required restriction under 35 U.S.C. § 121 between:

Group I, claims 1-33, characterized by the Examiner as being drawn to multiplexing signals;

Group II, claims 34-69, characterized by the Examiner as being drawn to de-multiplexing signals; and

Group III, claims 70-111<sup>1</sup>, characterized by the Examiner as being drawn to multiplexing combined with de-multiplexing.

Applicants provisionally elect to prosecute Group III, claims 70-111.

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<sup>1</sup> The Office Action indicates that Group III includes claims 17-111. Claims 17-33, however, are included in Group I and claims 34-69 are included in Group II. Accordingly, Applicants assume that the Examiner intended to only include claims 70-111 in Group III.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 50-2022.

Respectfully submitted,

Dated: June 27, 2008

By: /David L. Soltz/  
David L. Soltz  
Reg. No. 34,731  
(240) 264-5458